ASSET-TRACKING Workshop prerequisites

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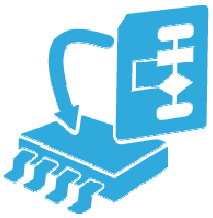
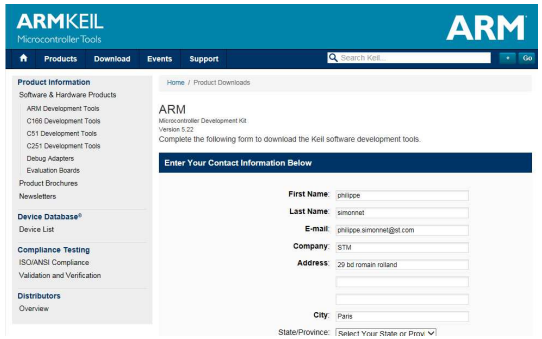
Keil installation (1/3)

- ARM/Keil workbench installation (STM32L0/F0/BlueNRG-1/2) :

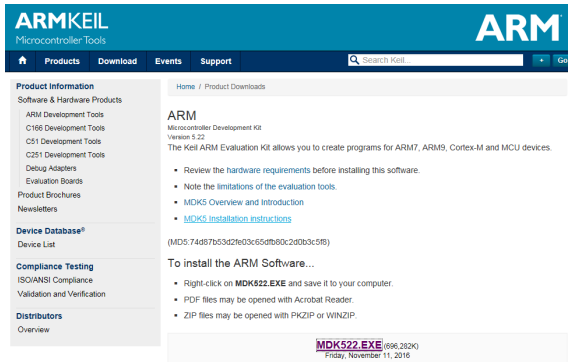
1. https://www.keil.com/demo/eval/arm.htm
   - Free of charge for ST Cortex M0/M0+
   - > (BlueNRG-1/2 , STM32L0/F0)



2. Fill the form :



3. And download ARM Software install :



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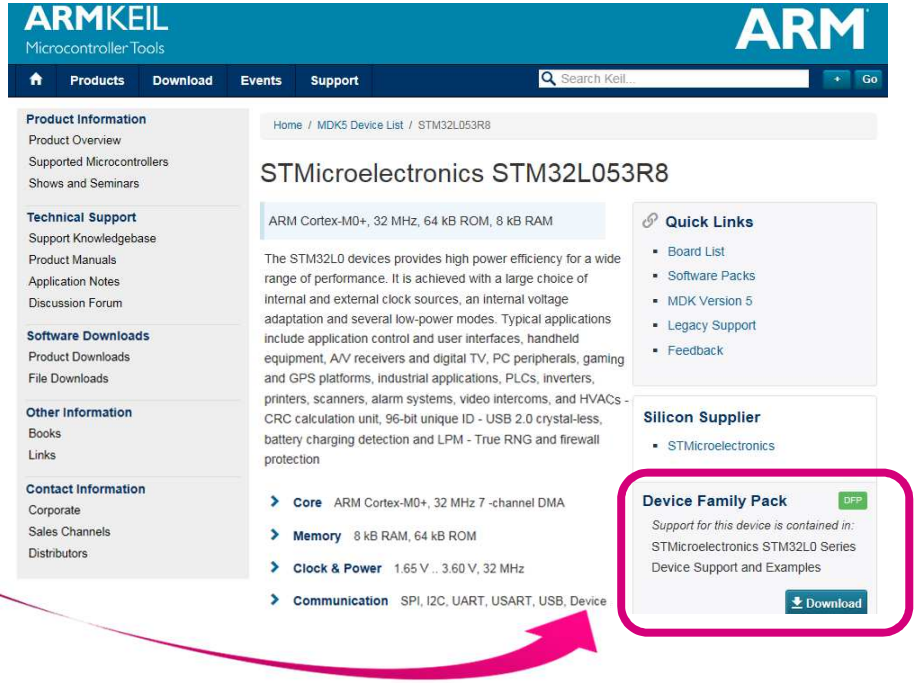
Keil installation (2/3)

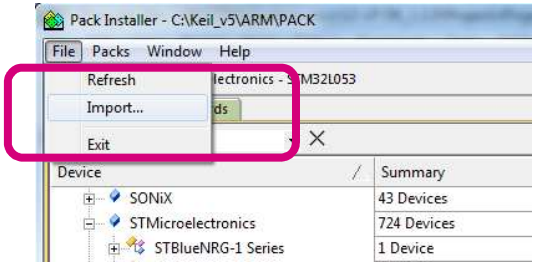
4 For S2-LP Hands On - Check for STM32L0 package update :

- http://www.keil.com/dd2/stmicroelectronics/stm32l053r8/#/eula-container

5 And download device family Pack:



6 Import pack installer once downloaded (zip file)



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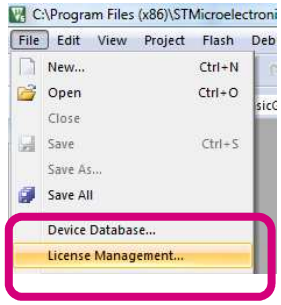
Free license
ST Cortex M0+

Keil installation (3/3)

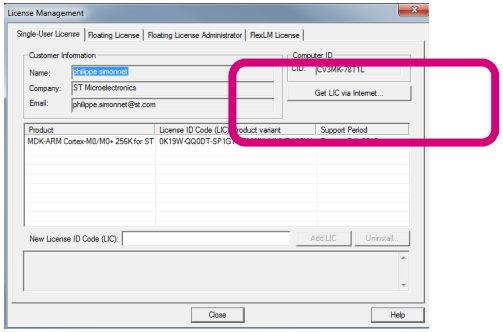
4 Go on keil webpage to get PSN (Product Serial Number) :

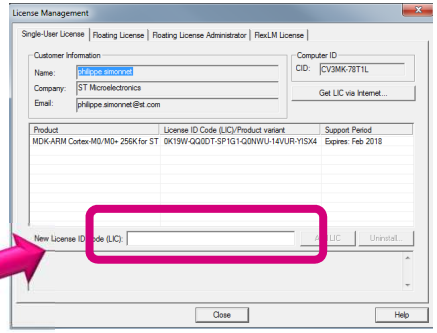
- To get PSN (Product Serial Number)
- http://www2.keil.com/stmicroelectronics-stm32/mdk

- Go to Keil IDE license management



- Click on ‘Get a LIC via Internet’ button





- Fill the form and you will receive a mail with new license ID

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SW tools prerequisites
ST-Link driver

Download these 2 packs

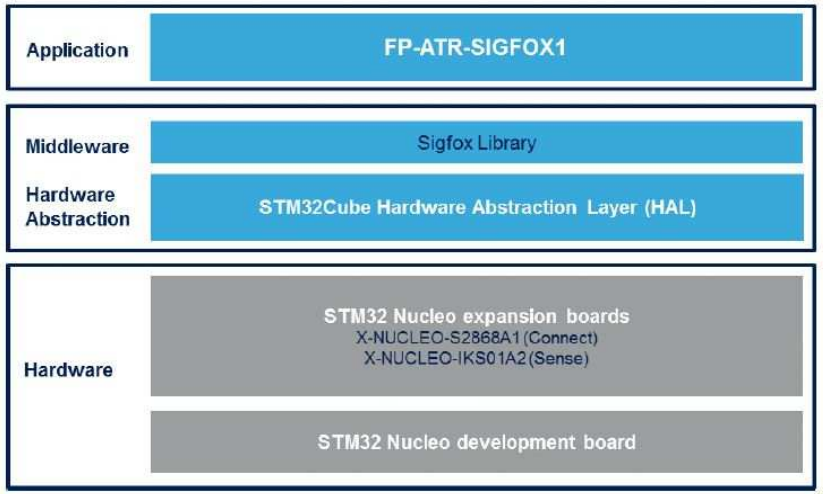
1 **ST-Link/V2-1** : http://www.st.com/en/embedded-software/stsw-link009.html

- ST-Link embedded in Nucleo board
- ST-Link USB driver required
- Purposes :
  - Flash & debug Nucleo boards
  - Debug S2-LP applications

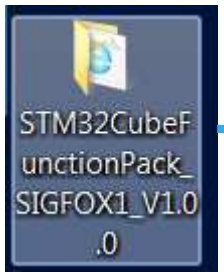
2 **FP-ATR-SIGFOX1** : Download on this page

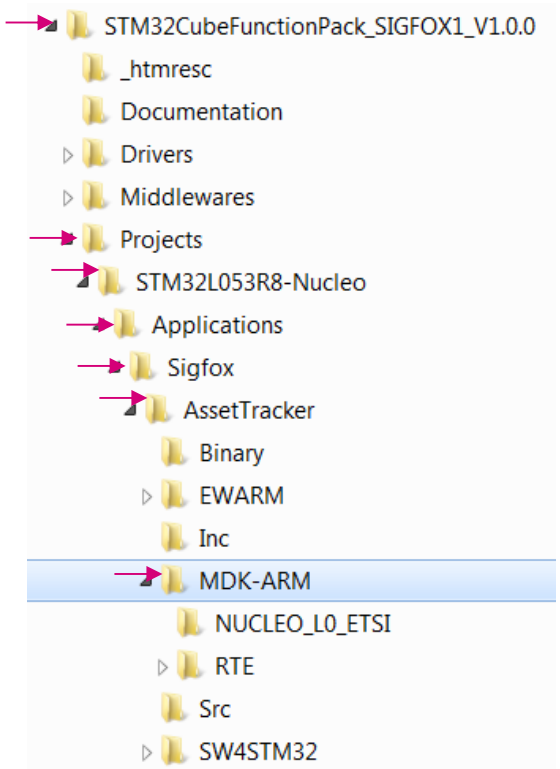
- STM32Cube function pack with MEMS sensor & SigFox connectivity
- Purposes :
  - Show Asset tracking solution
  - Will be used in the workshop.

Unzip the package on the Desktop



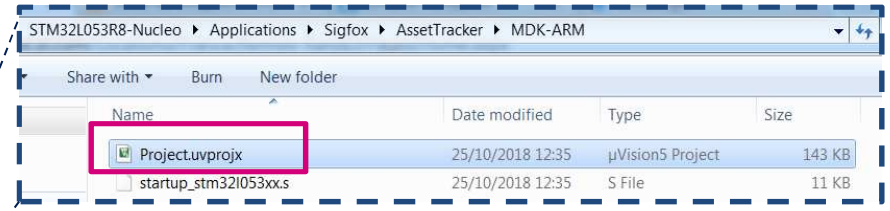
Verify that Keil can build the project





1. Open the folder and go to the **MDK-ARM** Folder :
   STM32CubeFunctionPack_SIGFOX1_V1.0.0\Projects\STM32L053R8-Nucleo\Applications\Sigfox\AssetTracker\MDK-ARM

2. Open (double-click) the « ***Project.uvprojx*** » file, with Keil.



3. Clean the target : « *Project > Clean Targets…* »
   Build the project : « *Project > Build Target …* »
   Build Log should display 0 error & 0 warning. => Compil. is OK.

```
compiling stm32l0xx_hal_msp.c...
linking...
Program Size: Code=44328 RO-data=1076 RW-data=804 ZI-data=3164
FromELF: creating hex file...
After Build - User command #1: fromelf --bin -o .\NUCLEO_L0_ETSI\NUCLEO_L0_ETSI.bin
"NUCLEO_L0_ETSI\NUCLEO_L0_ETSI.axf" - 0 Error(s), 0 Warning(s).
Build Time Elapsed:  00:01:41
```